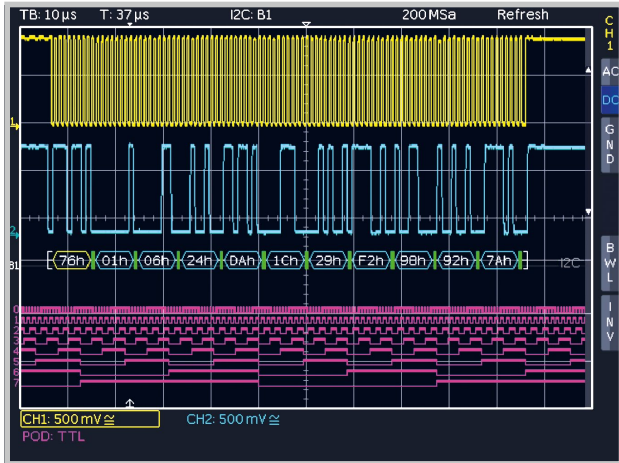
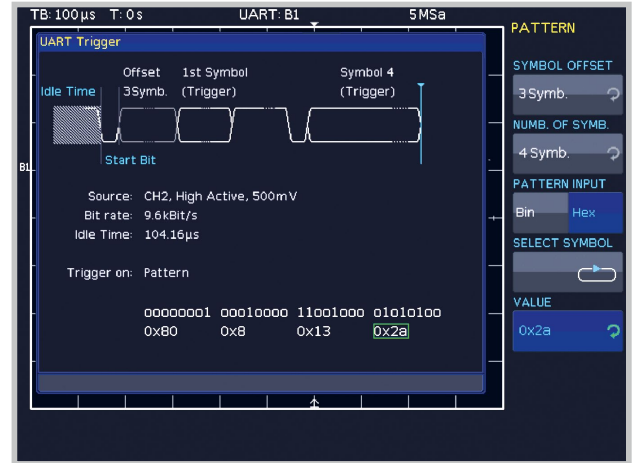


H0011 Serial Bus

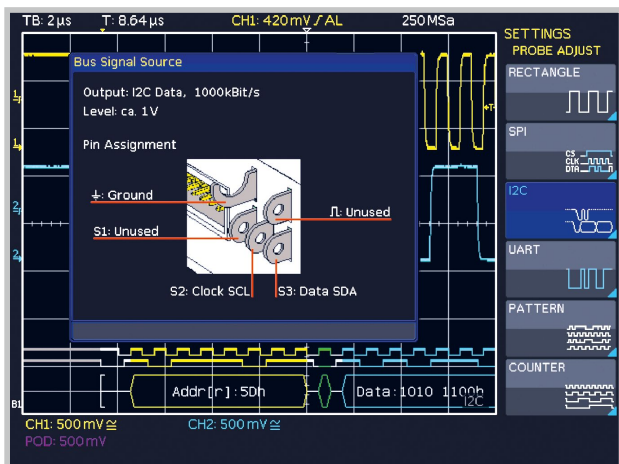
for all Oscilloscopes of the HMO Series



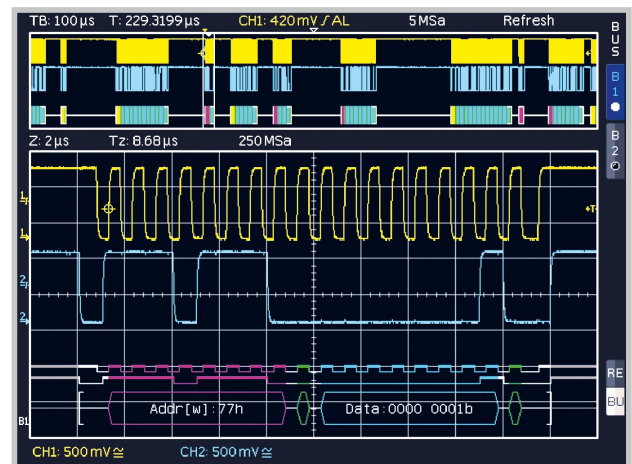
Mixed Signal and Bus Display



UART Bus Trigger Setup



Setting of the internal Bus Signal Source of the HMO



I²C Bus Decode and Binary with Zoom

- ☑ I²C, SPI, UART/RS-232 Bus Trigger and Decode
- ☑ Hardware accelerated Decode in Realtime
- ☑ Color Coded Display of the Content for intuitive Analysis and easy Overview
- ☑ More Details of the decoded Values become visible with increasing Zoom Factor
- ☑ Bus Display with synchronous Display of the Data and maybe Clock Signal
- ☑ Decode into ASCII, Binary, Hexadecimal or Decimal Format
- ☑ Up to four Lines to comfortably show the decoded Values
- ☑ Powerful Trigger to isolate specific Messages
- ☑ Option for all Oscilloscopes of the HMO Series, retrofittable

H0011

H0011 Serial Bus

CAN/LIN Trigger and Decode in Progress

I²C Bus

SPI Bus

UART/RS-232 Bus

Bus Configuration

Bit/Baud rate	up to 10Mbit/s (HMO352x/2524)*, up to 5Mbit/s (HMO72x...202x)	up to 25Mbit/s (HMO352x/2524)*, up to 12.5Mbit/s (HMO72x...202x)	300, 600, 1200, 2400, 4800, 9600, 19200, 38400, 57600, 115200 Baud, up to 62.5Mbit/s (HMO352x/2524)*, up to 31Mbit/s (HMO72x...202x)
Number of Bit's	7 or 10Bit for Address ID 8Bit for Data	32Bit for Data	8Bit for Data 1, 1.5, 2Bit for Stop Bit
Polarity	n/a	Chip select, positive or negative, or without Chipselect (2-wire SPI) Clock rising or falling edge Data High or Low active	High or Low active
Parity	n/a	n/a	none, odd or even

Trigger

Source	analog Channel LCH 1...2 [CH 1...4]	analog Channel LCH 1...2, external Trigger Entry for Chip Select, [CH 1...4]	analog Channel LCH 1...2 [CH 1...4]
Event	7 or 10Bit Address ID 7 or 10Bit Address ID with 8Bit Data Start, Stop, Restart missing Acknowledge Address ID without Acknowledge	Data packets up to 32Bit with positive or negative Chip Select or without Chip Select, [2-wire SPI]	Data packets up to 8Bit
Input format	Hexadecimal or Binary	Hexadecimal or Binary	Hexadecimal or Binary

Hardware accelerated Decode

Source	analog Channel LCH 1...2 [CH 1...4]	analog Channel LCH 1...2, external Trigger Entry for Chip Select, [CH 1...4]	analog Channel LCH 1...2 [CH 1...4]
Display	Bus display, color coded for Read Address ID: Yellow Write Address ID: Magenta Date: Cyan Start: White Stop: White ACK/NACK: Green/Red Error: Red Trigger Condition: Green up to four lines for decoded values, synchronous display of the Bit lines	Bus display, color coded for Date: Cyan Start: White Stop: White Error: Red Trigger Condition: Green up to four lines for decoded values, synchronous display of the Bit lines	Bus display, color coded for Date: Cyan Start: White Stop: White Error: Red Trigger Condition: Green up to four lines for decoded values, synchronous display of the Bit lines
Format	Address ID: hexadecimal Data ASCII, binary, decimal, hexadecimal	n/a Data ASCII, binary, decimal, hexadecimal	n/a Data ASCII, binary, decimal, hexadecimal

*Available for HMO352x/2524 from firmware 3.0